



Title: APPARATUS AND METHOD FOR NOISE REDUCTION FOR A
SUCCESSIVE APPROXIMATION ANALOG-TO-DIGITAL CONVERTER CIRCUIT
First Inventor: Vadim Tsinker
Attorney Docket No.: 08211/0200388-US0/P05834

1/16

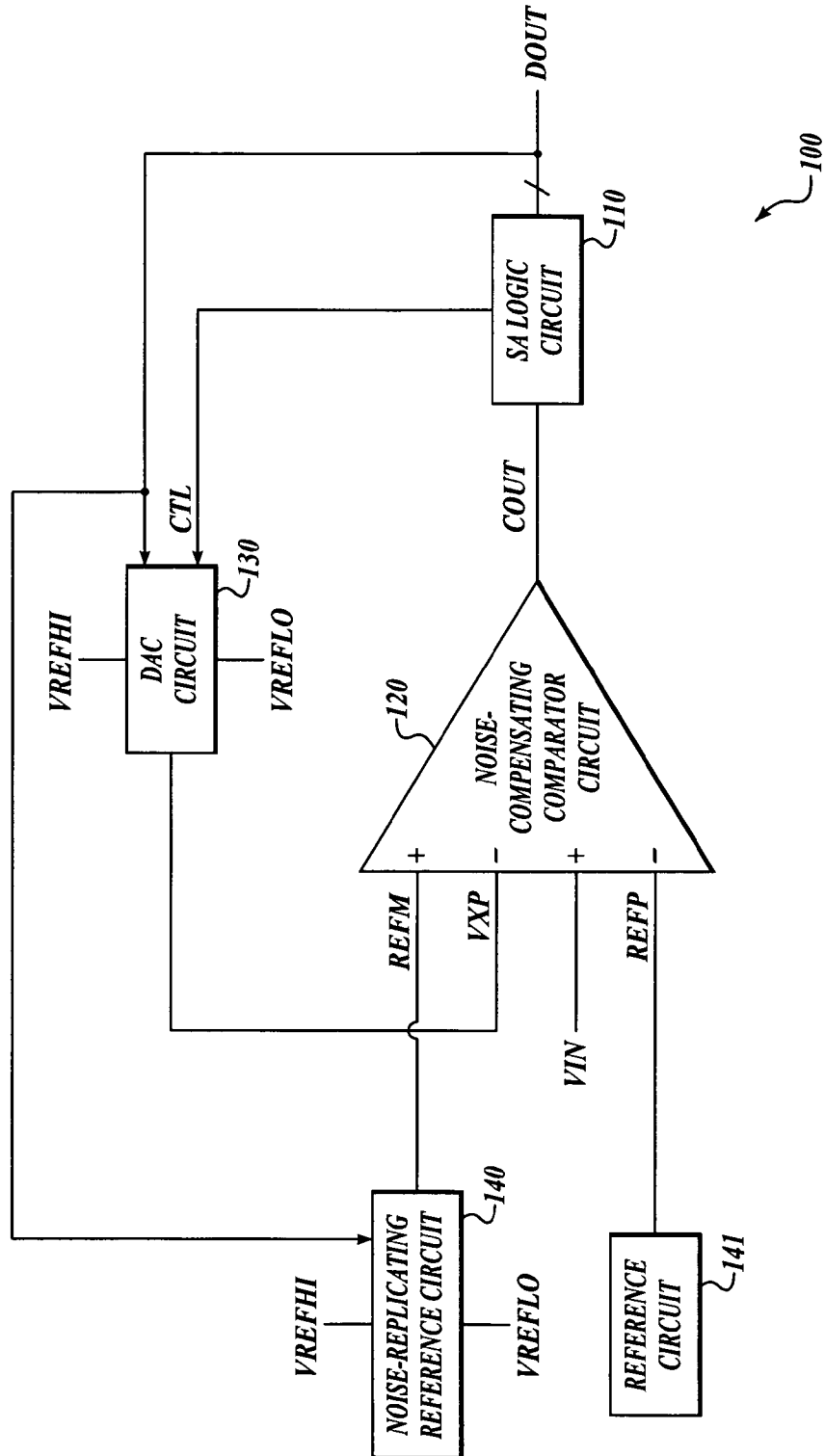


Fig. 1



2/16

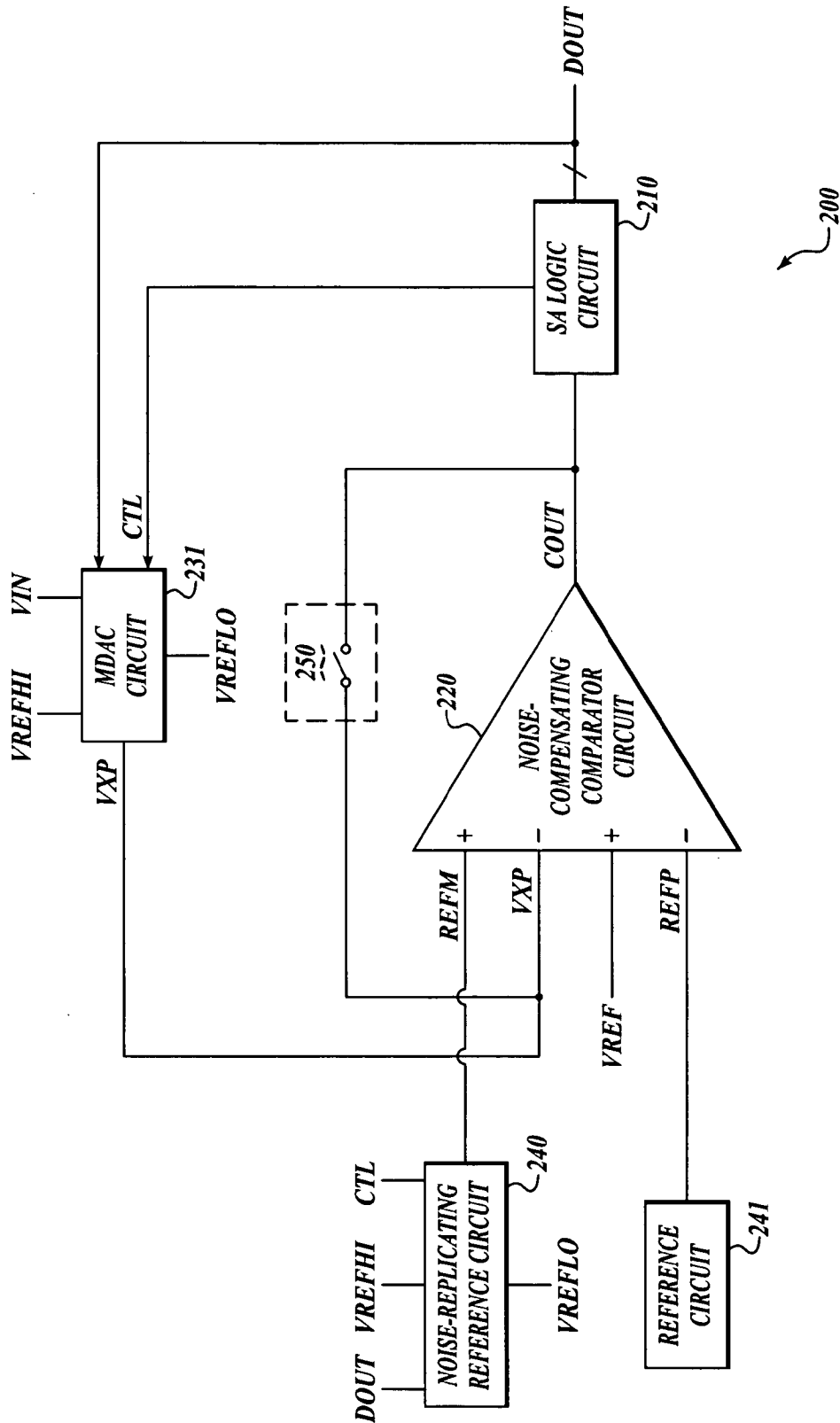
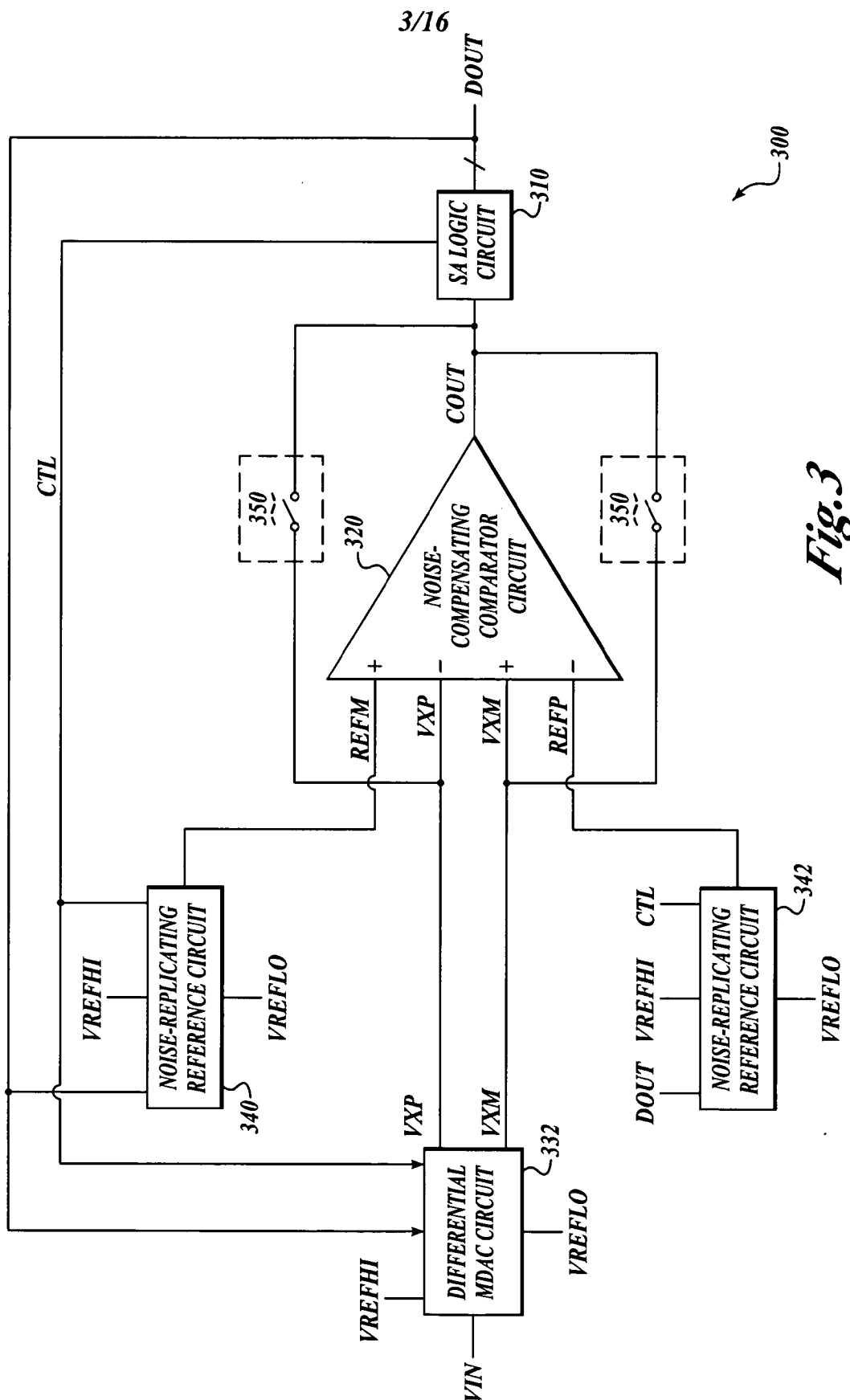


Fig. 2



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4/16

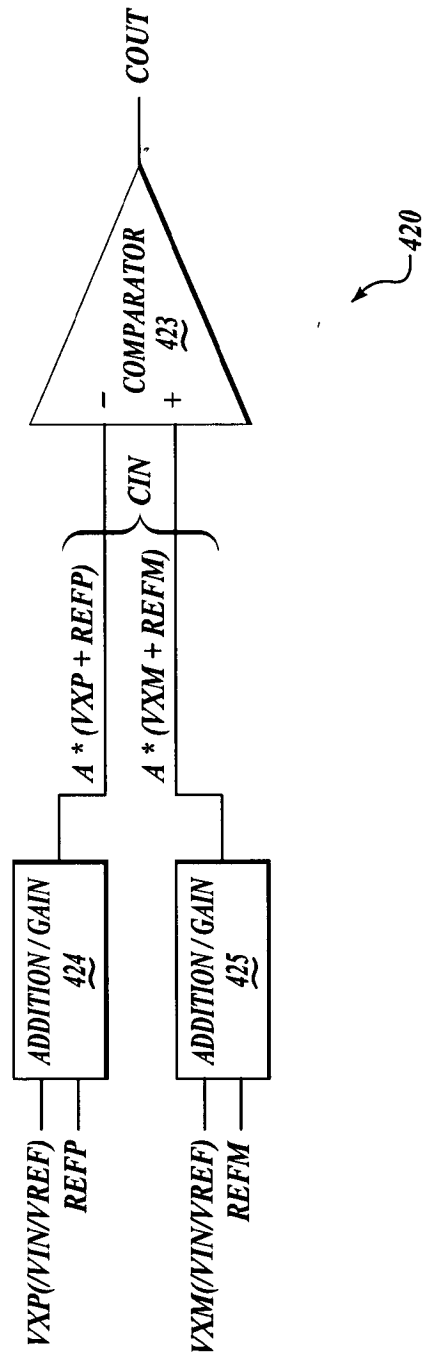


Fig. 4

5/16

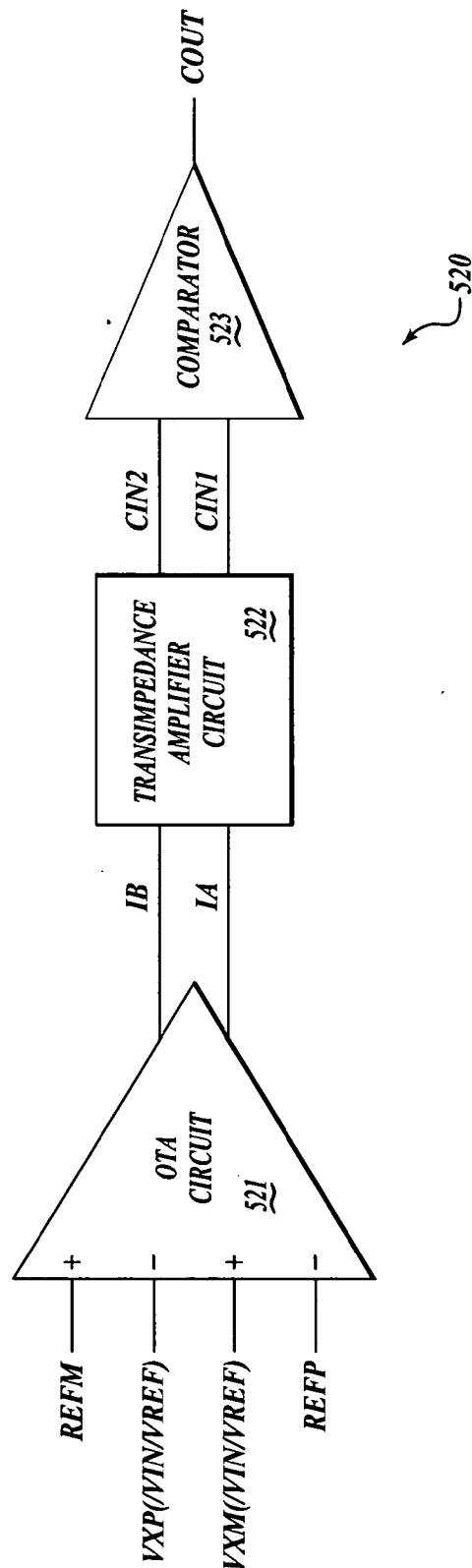


Fig. 5

6/16

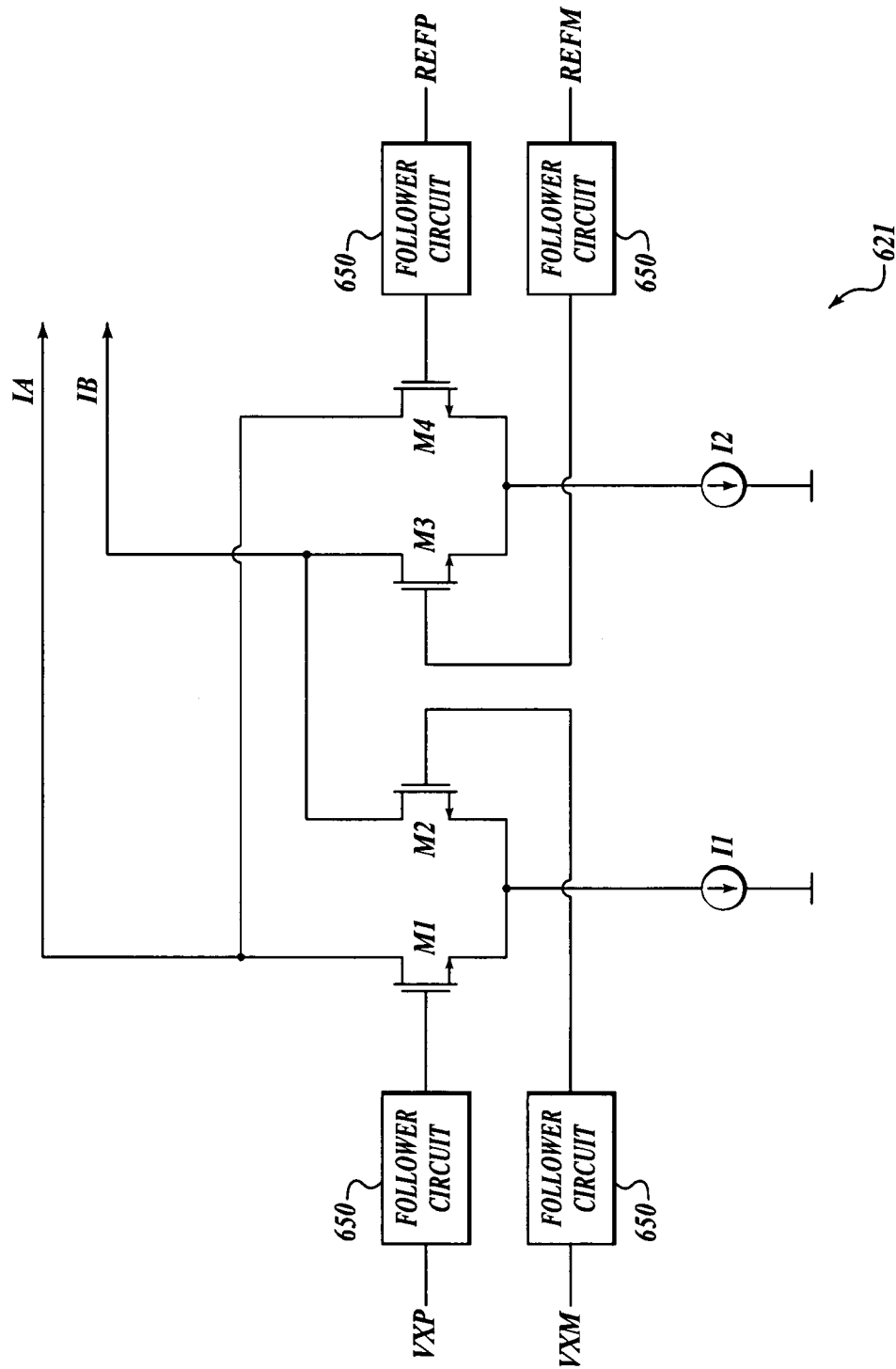


Fig. 6

7/16

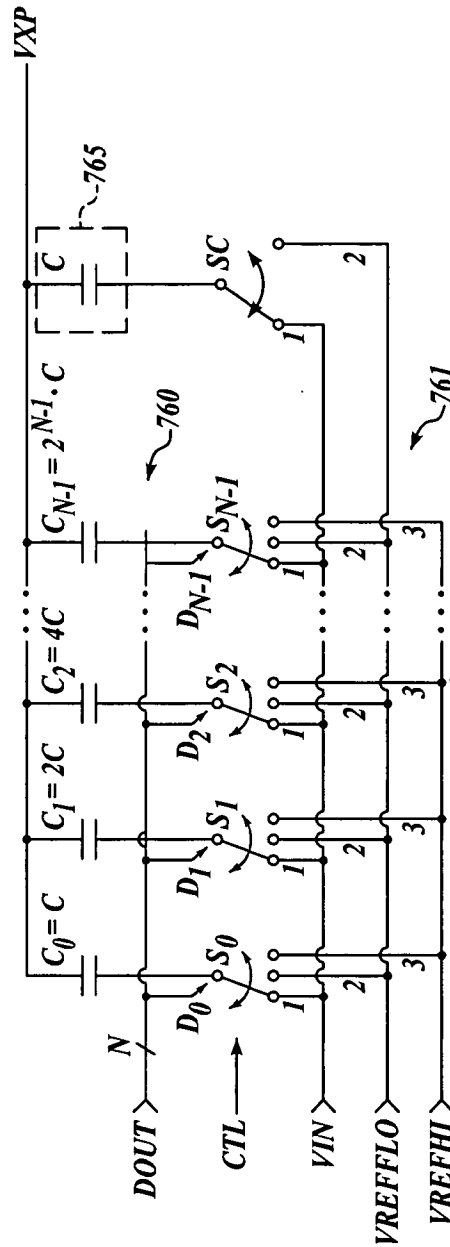


Fig. 7

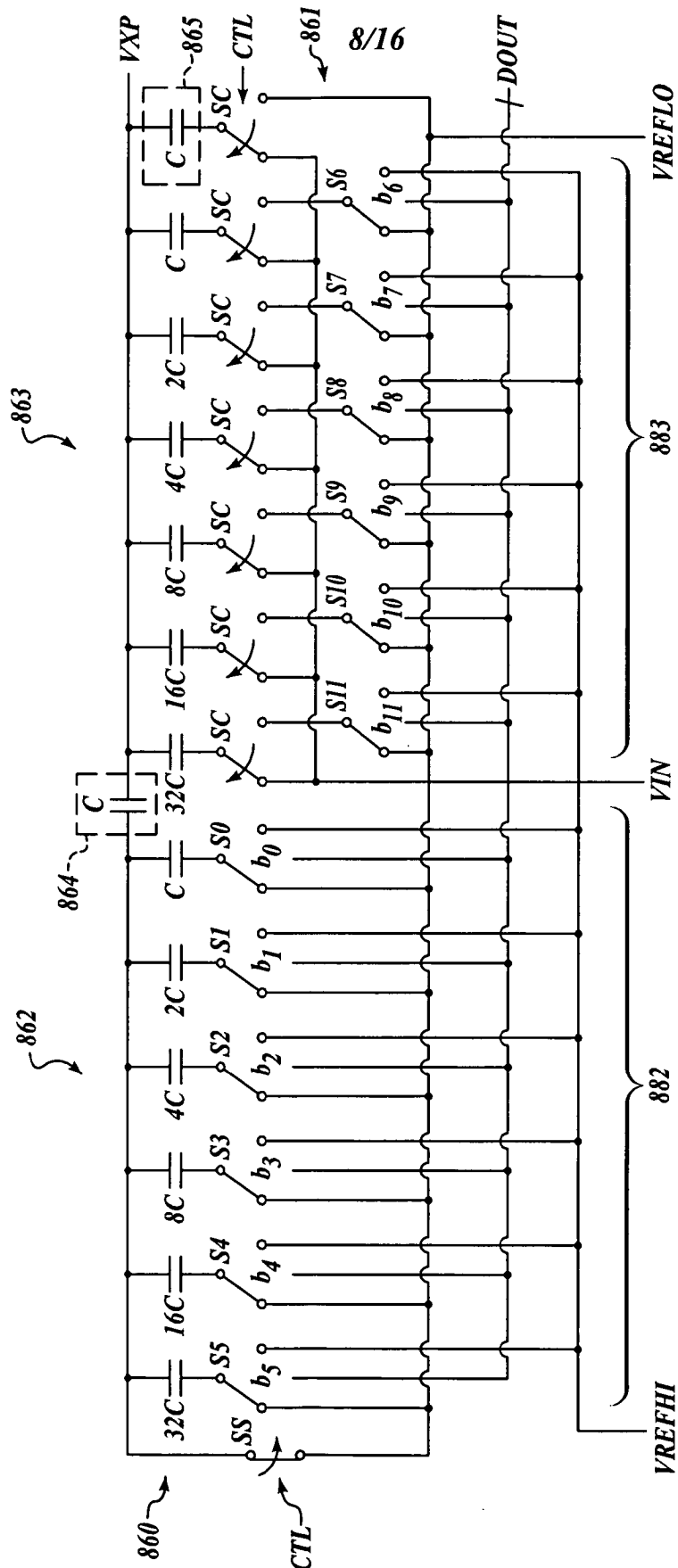


Fig. 8

9/16

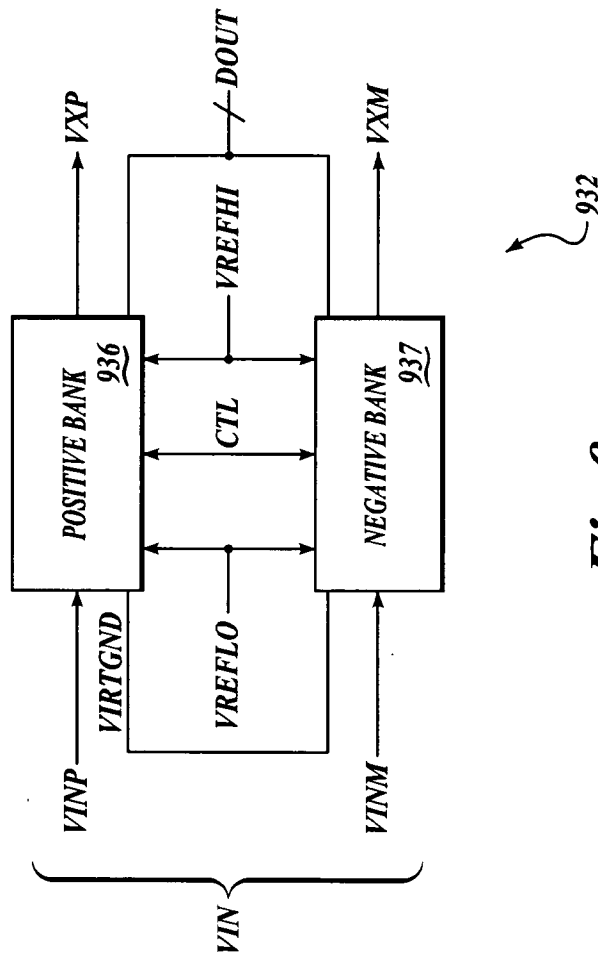


Fig. 9

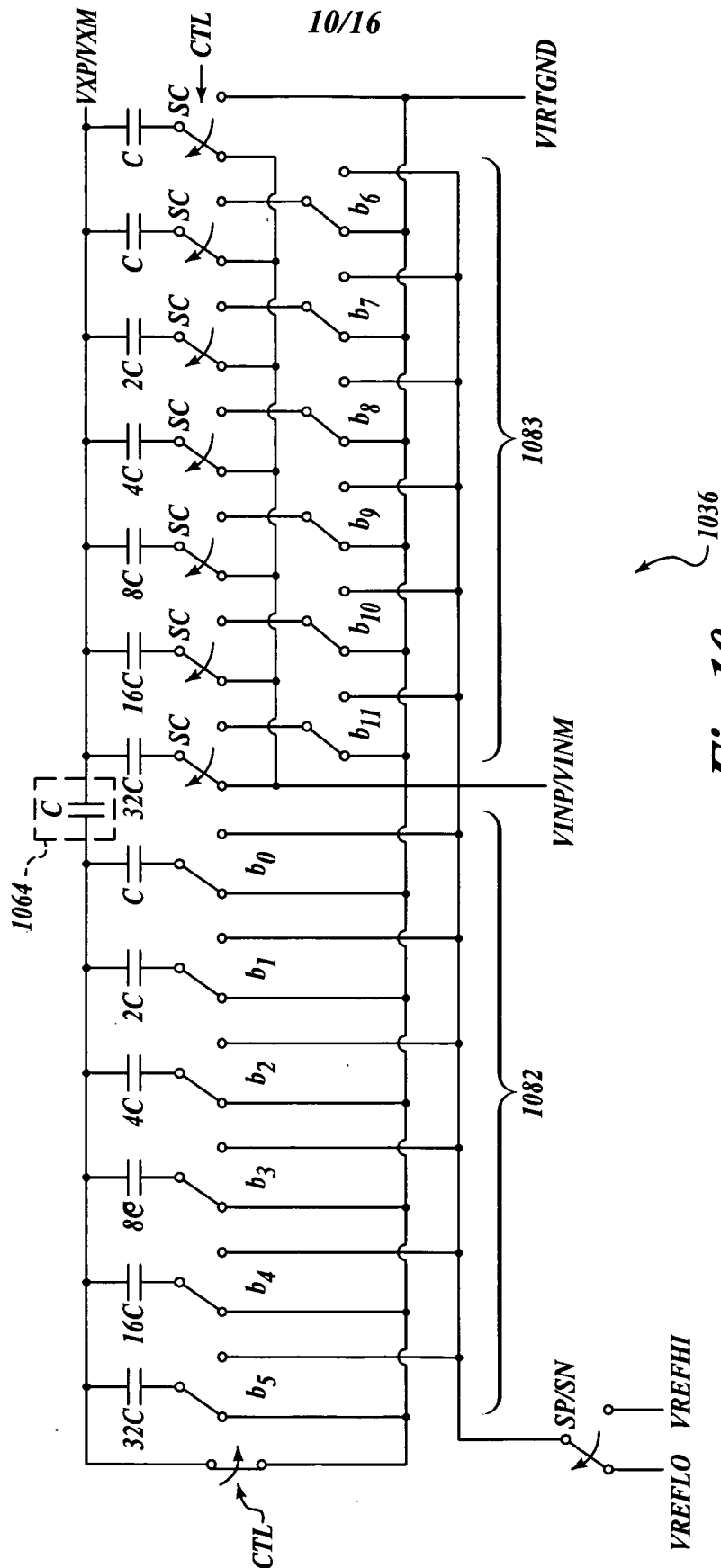


Fig. 10

11/16

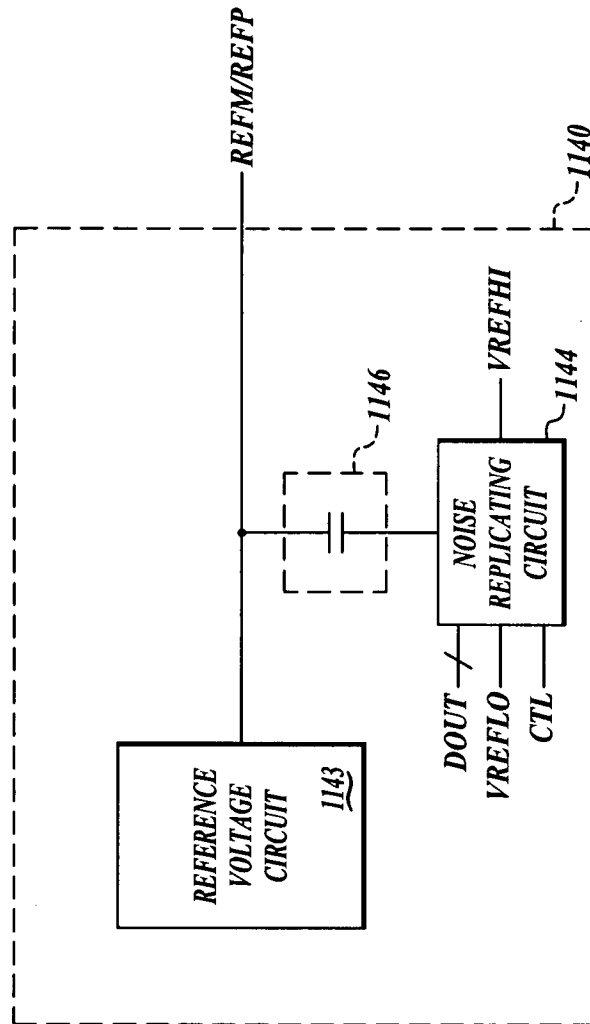


Fig. 11A

12/16

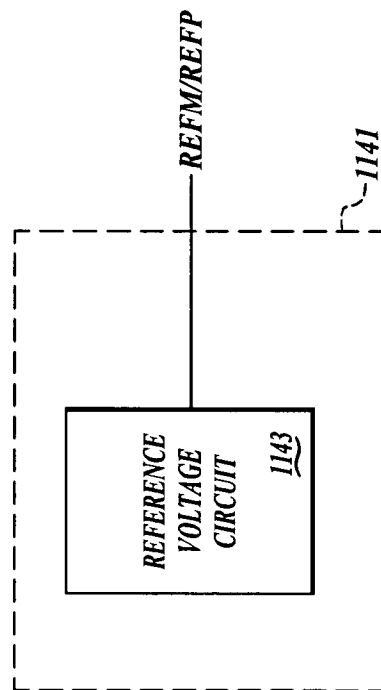


Fig. 11B

13/16

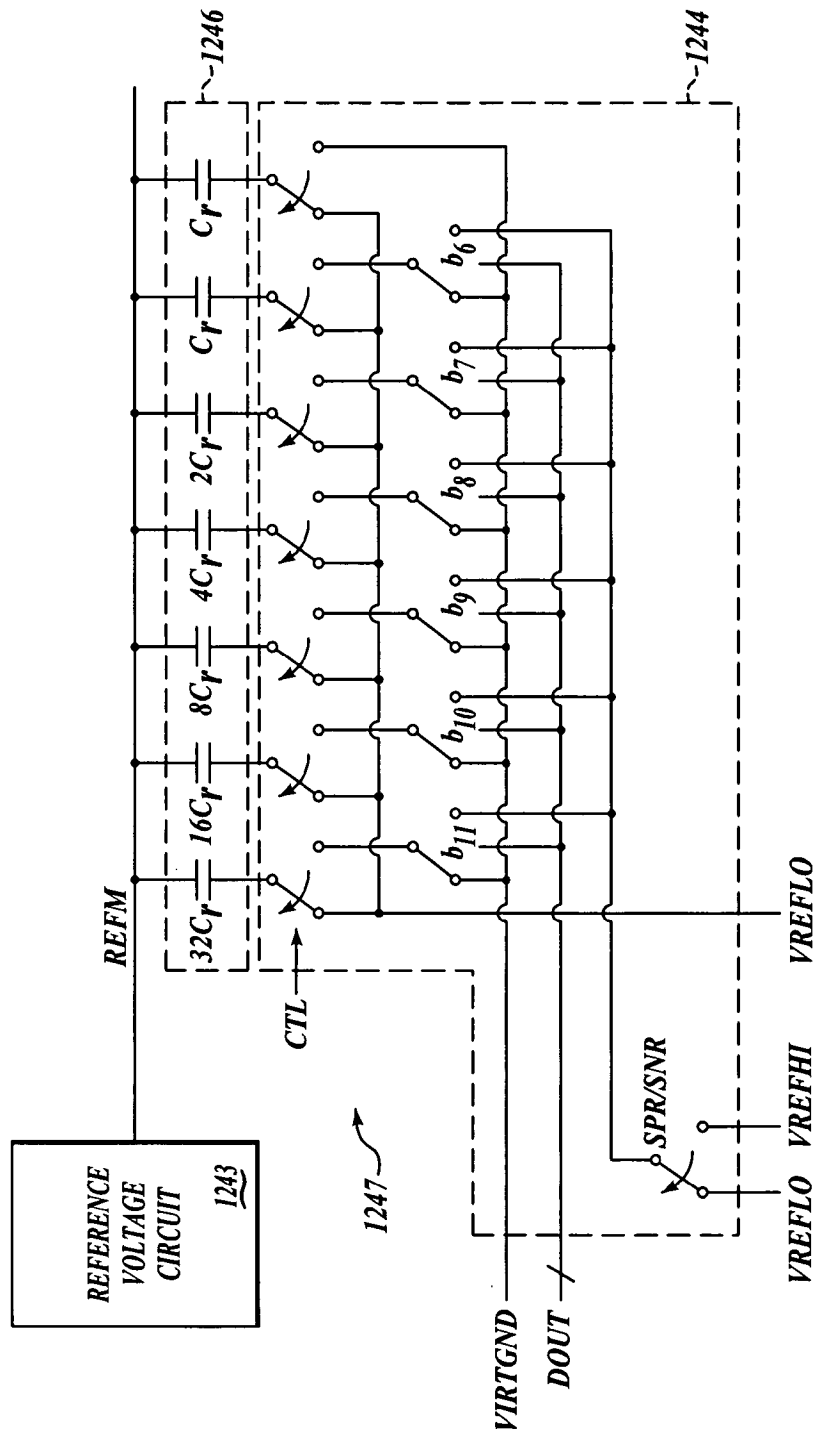


Fig. 12

14/16

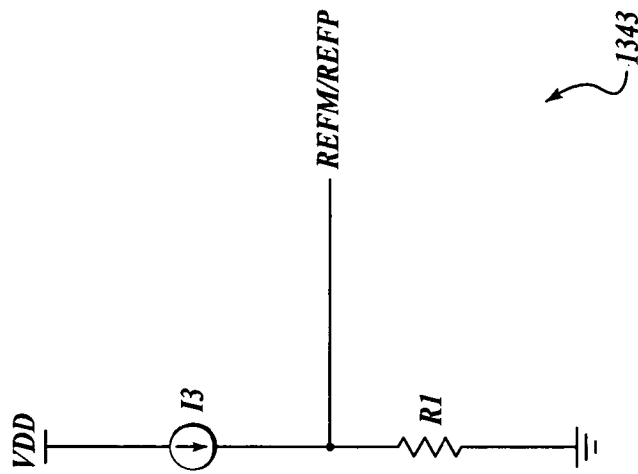


Fig. 13

15/16

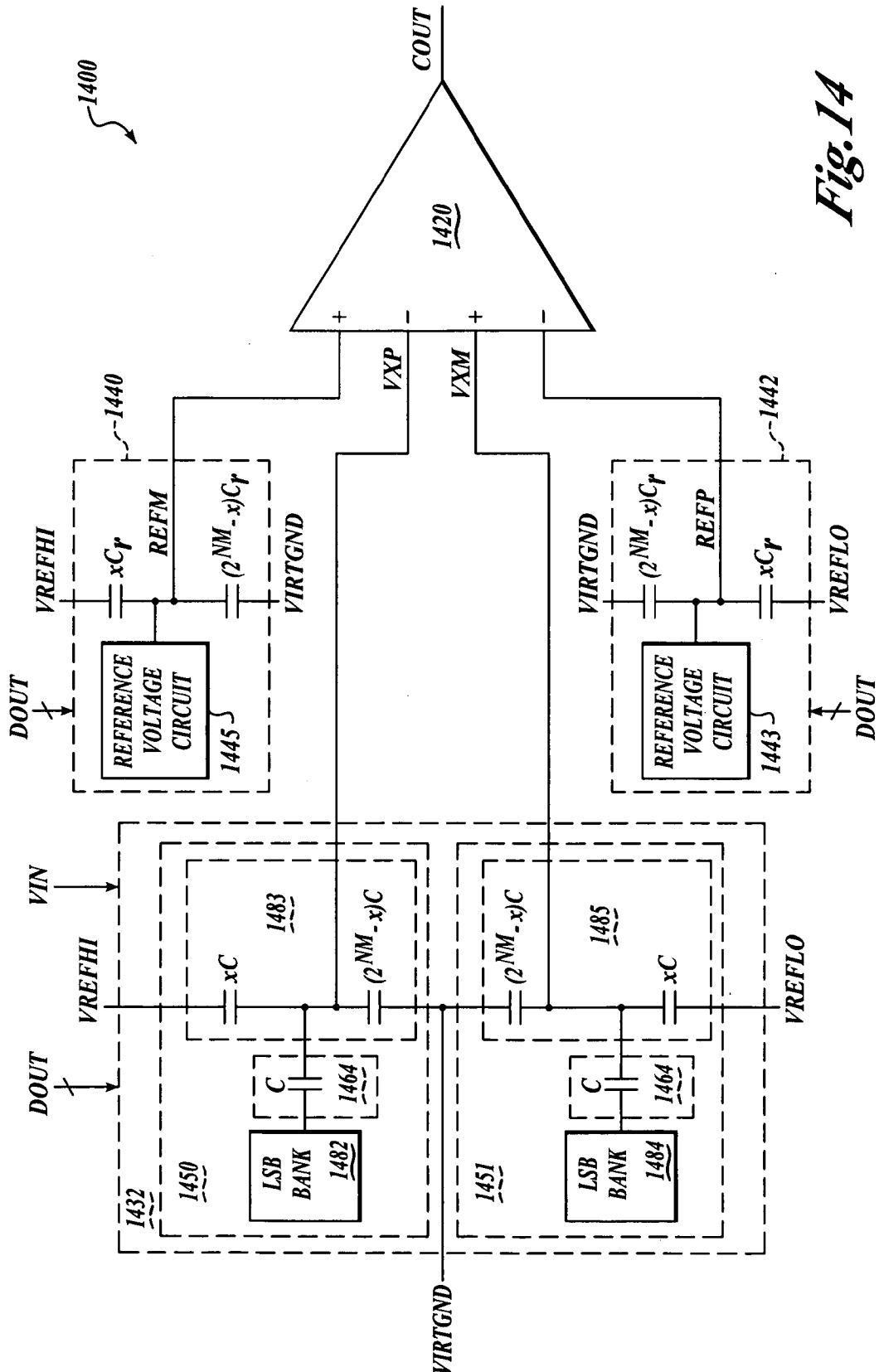


Fig. 14

16/16

